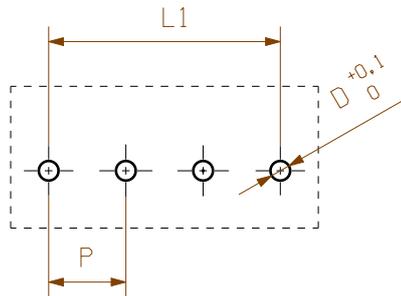
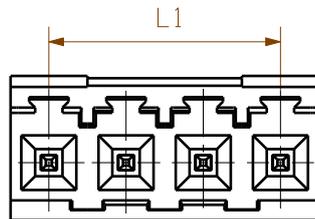
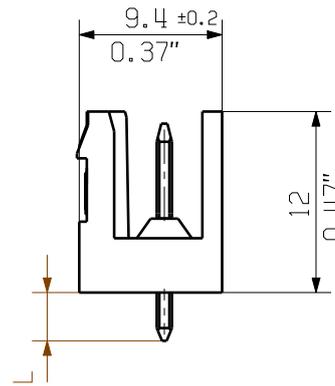
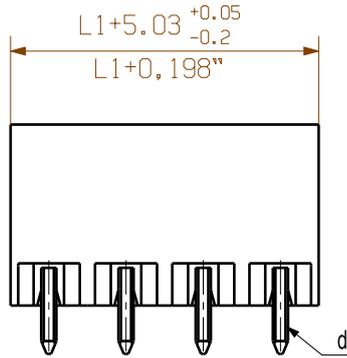


The reproduction, distribution and utilization of this document as well as the communication of its contents without explicit authorization is prohibited. Offenders will be held liable for the payment of damages. Weidmüller exclusively reserves the right to file for patents, utility models or designs.

© Weidmüller Interface GmbH & Co. KG



HOLE PATTERN

For the mounting of PCBs, it should be noted that the rated data relates only to the PCB components alone. The necessary creepage and clearance paths must be observed in connection with the respective applicant in accordance to IEC 664 / VDE 0110. The current-carrying capacity and pitch tolerance is to be determined according to DIN IEC 326 part 3 very fine.

Weidmüller PCB components are tested to the DIN EN 61984 standard, and are valid for its field of application. Provided that the components are used to the intended purpose, all requirements with respect to the occurring of electrical, mechanical, thermic and corrosive stress will be satisfied.

P = 5.08mm/0.2inch

d = max.1.2mm/0.047inch
D = 1.3mm/0.51inch

24	116,84	4,600
23	111,76	4,400
22	106,68	4,200
21	101,60	4,000
20	96,52	3,800
19	91,44	3,600
18	86,36	3,400
17	81,28	3,200
16	76,20	3,000
15	71,12	2,800
14	66,04	2,600
13	60,96	2,400
12	55,88	2,200
11	50,80	2,000
10	45,72	1,800
9	40,64	1,600
8	35,56	1,400
7	30,48	1,200
6	25,40	1,000
5	20,32	0,800
4	15,24	0,600
3	10,16	0,400
2	5,08	0,200
n	L1 [mm]	L1 [Inch]

3,2	0,1
	-0,3
4,5	0,1
	-0,3
PINLÄNGE L PIN LENGTH L	TOLERANZ TOLERANCE
n	L1 [mm] L1 [Inch]

SHOWN: SLA 04/180

General tolerance:
DIN ISO 2768-mK



101264/4
19.02.18 HERTEL_S 00

Modification

Weidmüller

Cat.no.:
3 27739 13

Drawing no. Issue no.
Sheet 01 of 02 sheets



	Date	Name
Drawn	17.09.2009	HELIS_MA
Responsible		HERTEL_S
Checked	27.02.2018	HELIS_MA
Approved		LANG_T

SLA .. / 180...
STIFTELEISTE
PIN HEADER

Scale: 2:1

Supersedes: .

Product file: SLA

7123