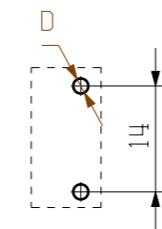
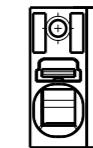
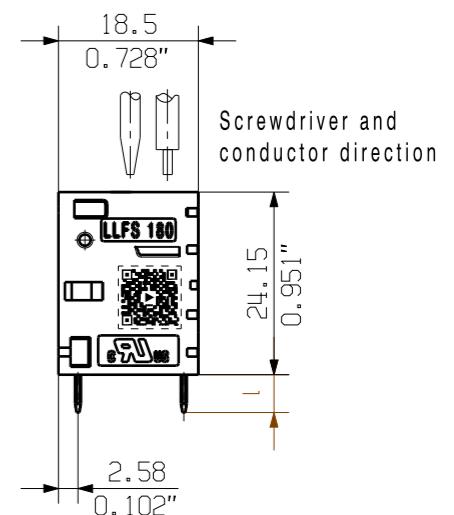
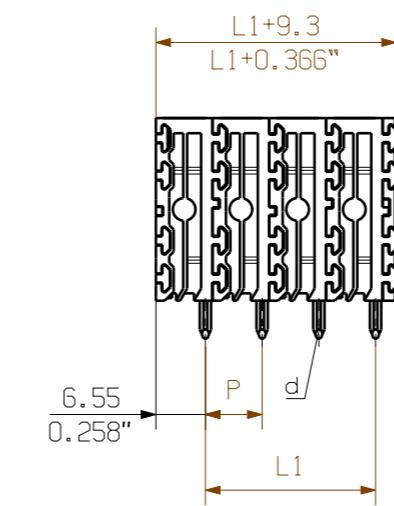
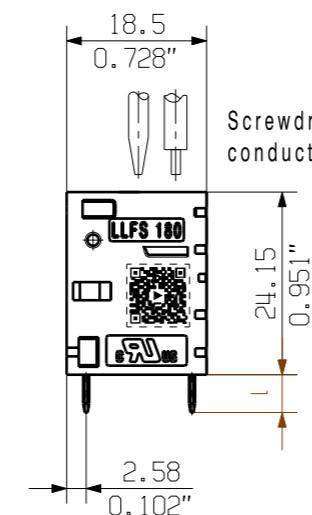
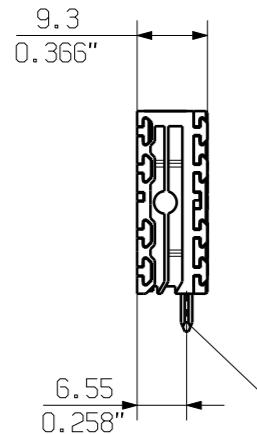
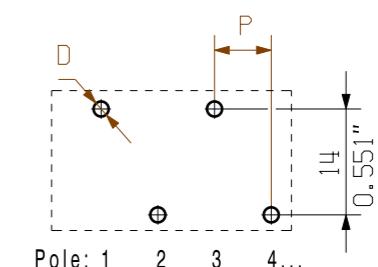
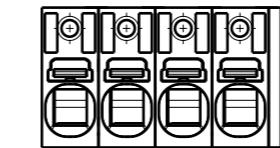


⑧

General customer drawing, topical version only if required



Hole pattern



Hole pattern

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For the mounting of PCBs, it should be noted that the rated data relates only to the PCB components alone. The necessary creepage and clearance paths must be observed in connection with the respective applicant in accordance with IEC 60664-1 (VDE 0110). The current-carrying capacity and pitch tolerance is to be determined according to DIN IEC 60326-3 very fine.

Weidmüller PCB components are tested to the IEC 60947-7-4 standard, and are valid for its field of application. Provided that the components are used to the intended purpose, all requirements with respect to the occurring of electrical, mechanical, thermic and corrosive stress will be satisfied.

General tolerance:  
DIN ISO 2768-mK

	EC00003706	1	Prim PLM Part No.: 337396		Prim ERP Part No.: 2491640000	
			Max. nos.		Modification	
			First Issue Date	22.09.2016		
					Date	Name
					Drawn	10.08.2020 Xiang, Keqin
					Responsible	Xiang, Keqin
	Scale: 1/1	Size: A3	Approved	28.08.2020	Xu, Shary	
	Drawings Assembly			Product file: 7416 LLF 7.50		

LLFS 7.50/.../180 ...  
LEITERPLATTENKLEMME  
PCB TERMINAL

64139

7

Issue no.

Sheet 01 of 01 sheets

$P = 7.50$	$0.295"$	(Pitch)
$D = 0.2 +0.1$	$0.079"$	
$d = 1.5 \times 0.8$	$0.059" \times 0.031"$	
$l = 5.0 +0.2 -0.6$	$0.197"$	
$n$	$L1$	$L1$
Poles	[mm]	[inch]