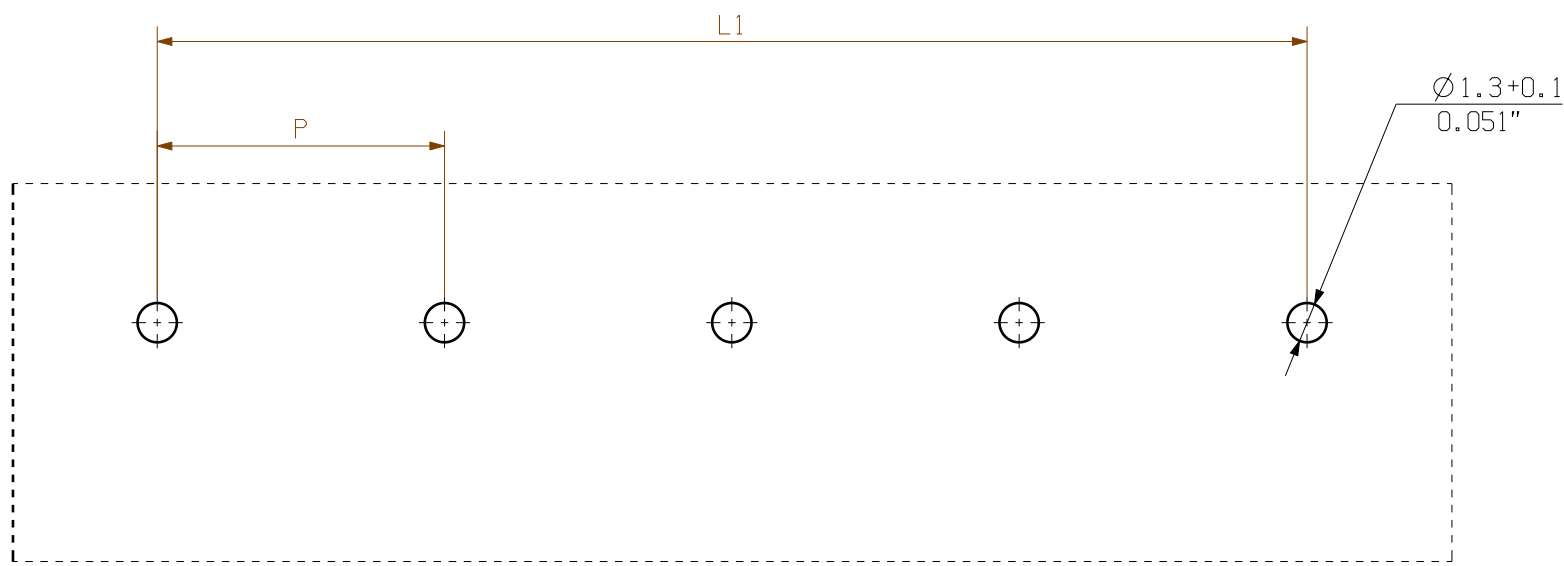
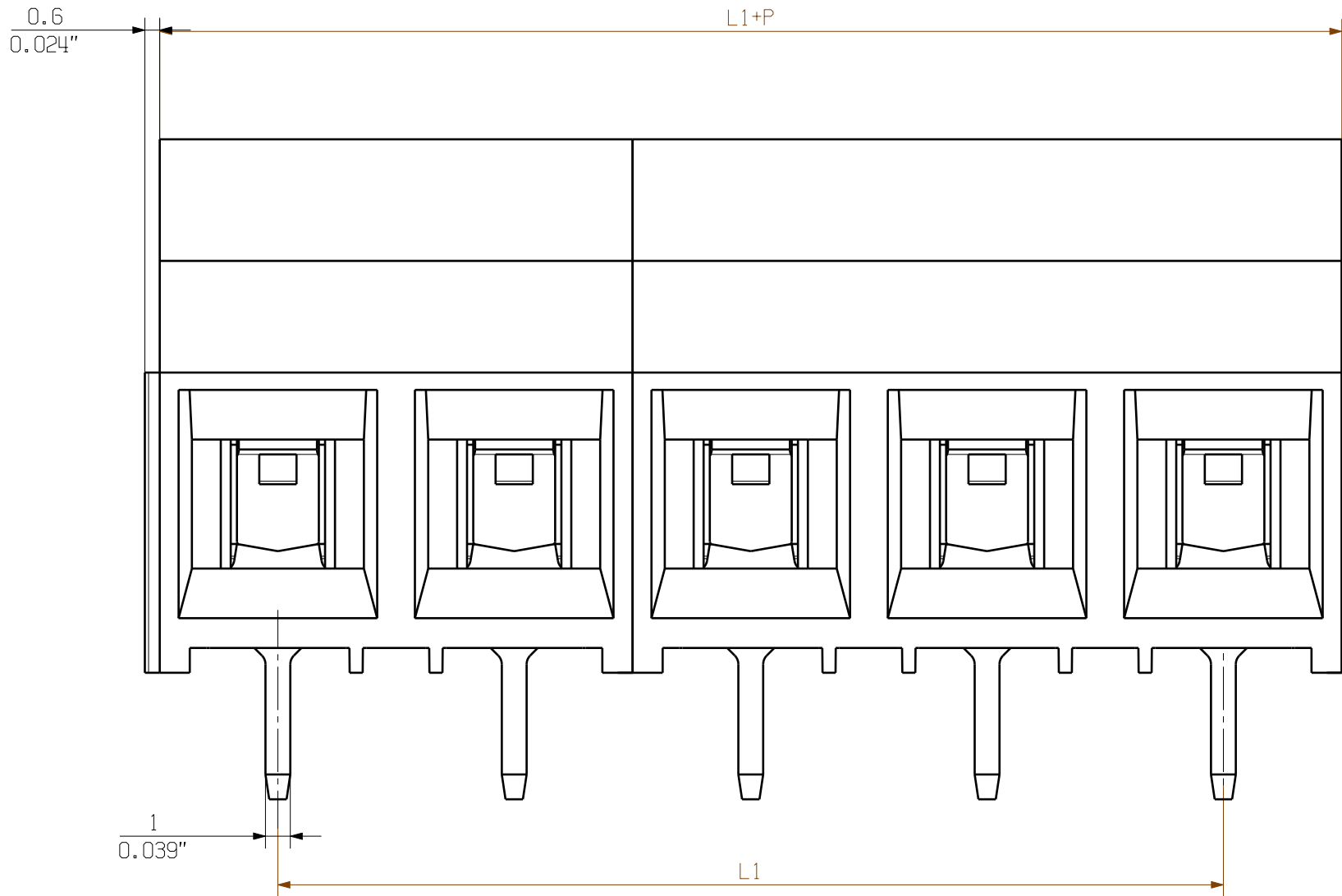
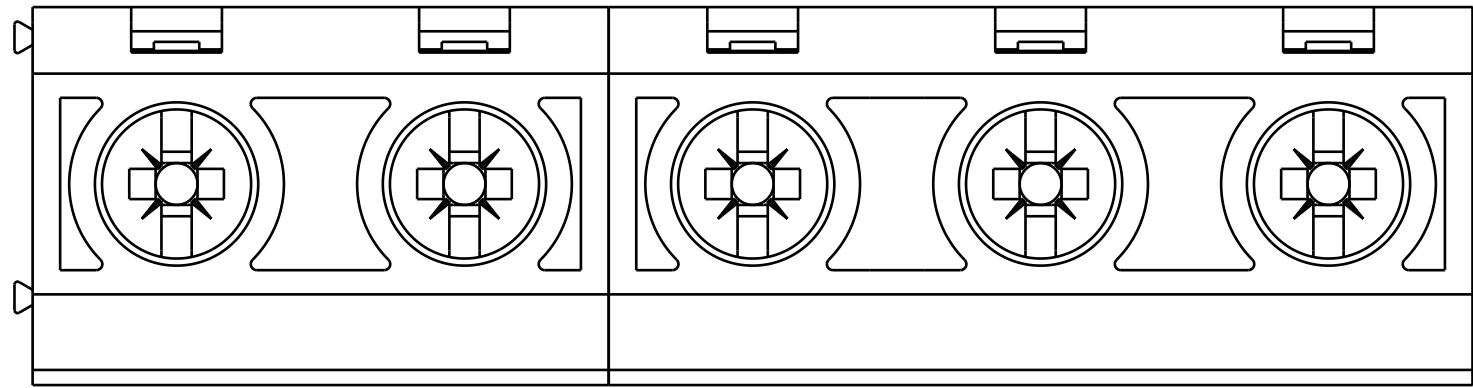
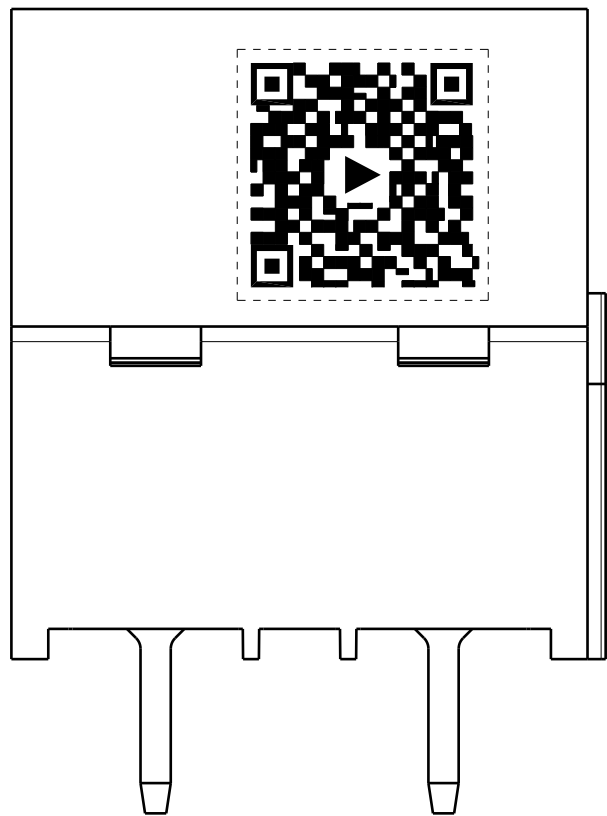
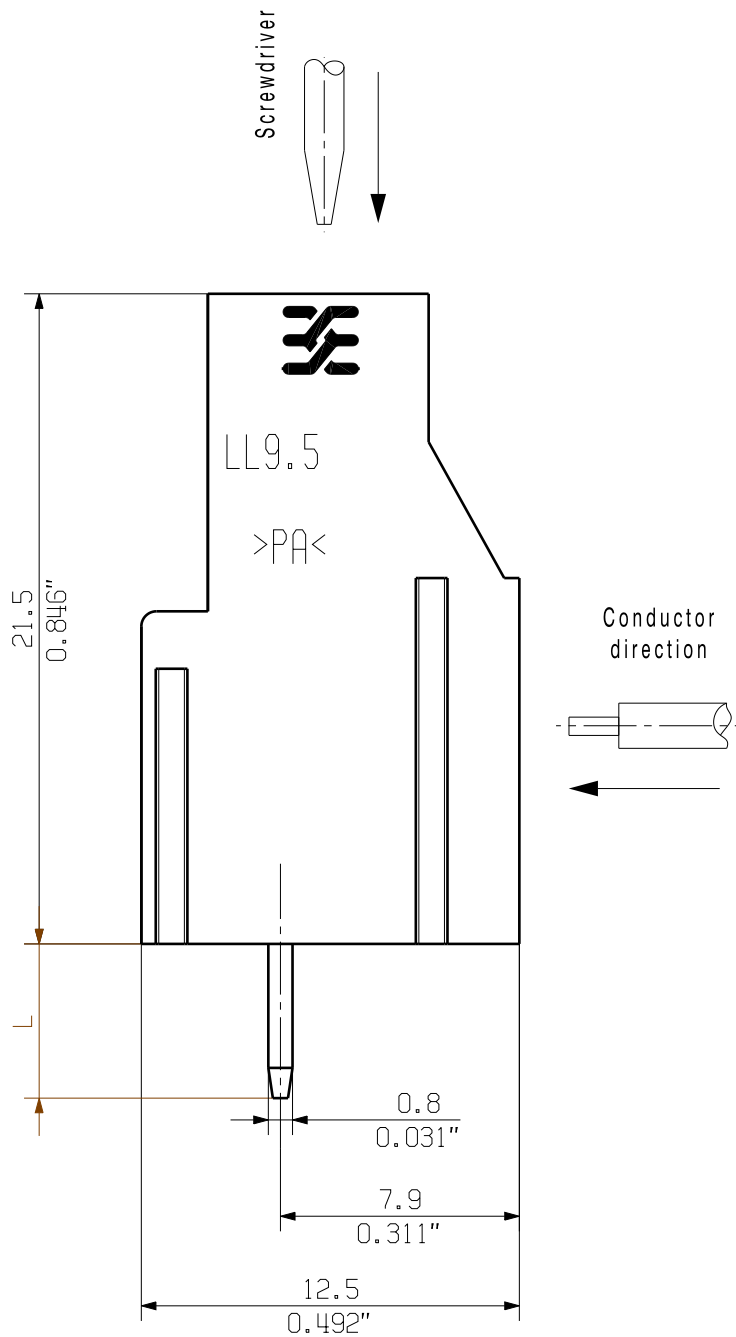


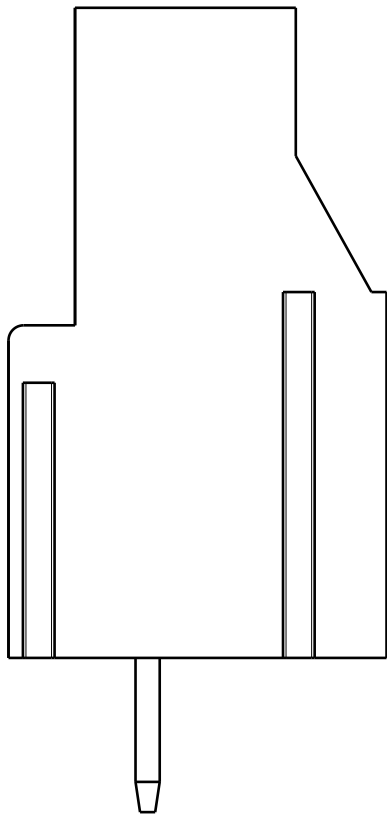


PCB LAYOUT

Customer drawing



Pin length L	Tolerance
5.0	0.10 -0.25



P= 9.52 Pitch
0.375

12	104.72	4.125
11	95.20	3.750
10	85.68	3.375
9	76.16	3.000
8	66.64	2.625
7	57.12	2.250
6	47.60	1.875
5	38.08	1.500
4	28.56	1.125
3	19.04	0.750
2	9.52	0.375
N	L1 [mm]	L1 [inch]
P	9.52 mm	0.375 inch

For the mounting of PCBs, it should be noted that the rated data relates only to the PCB components alone. The necessary creepage and clearance paths must be observed in connection with the respective applicant in accordance to IEC 60664-1 (VDE 0110). The current-carrying capacity and pitch tolerance is to be determined according to DIN IEC 60326-3 very fine.

Weidmüller PCB components are tested to the IEC 60947-7-4 standard, and are valid for its field of application. Provided that the components are used to the intended purpose, all requirements with respect to the occurring of electrical, mechanical, thermic and corrosive stress will be satisfied.

GENERAL TOLERANCE:
DIN ISO 2768-m

	EC00000683	00	Prim PLM Part No.: 026319			Prim ERP Part No.: 1912970000		
	First Issue Date 14.05.2018	Max. nos.				41724		
	Modification	Drawing no.				Issue no.		
		Date	Name	Sheet 01 of 01 sheets				
	Drawn	03.12.2018	Xiang, Kegin	LL 9.52/.../90 ... LEITERPLATTENKLEMME PCB TERMINAL				
	Responsible		Xiang, Kegin					
	Approved	04.12.2018	Xu, Shary					
Scale: 4/1		Size: A2		Product file: 7066 LL 9.52				
Drawings Assembly								