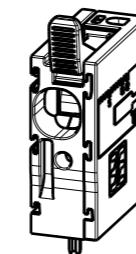
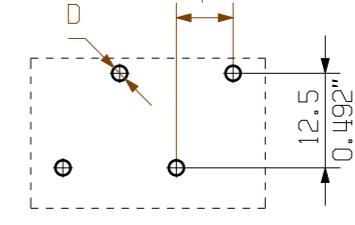
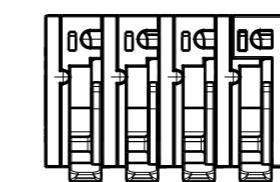
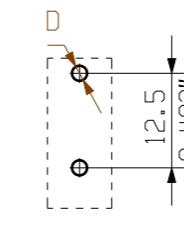
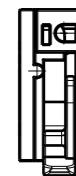
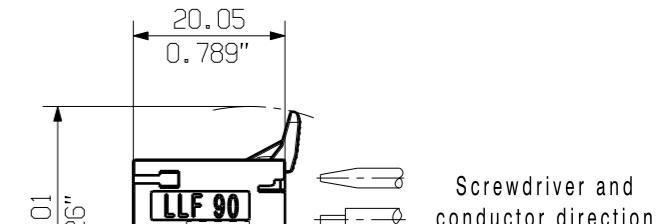
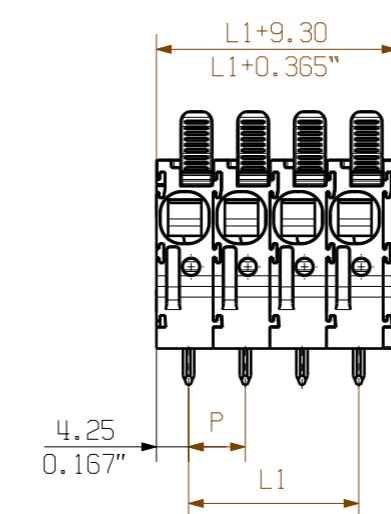
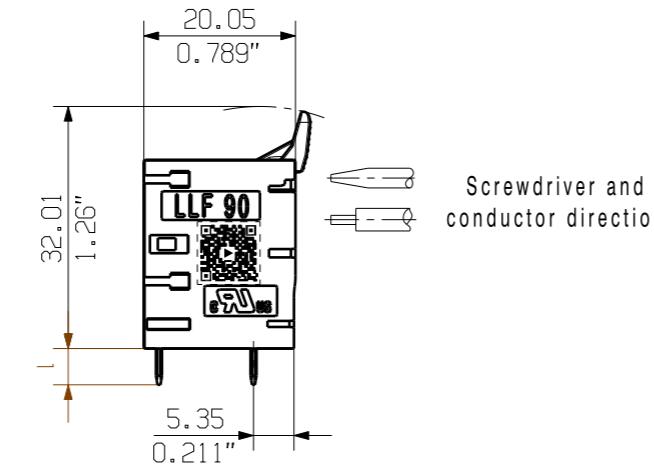
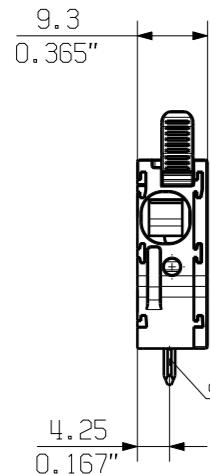


General customer drawing, topical version only if required



The dimensions and tolerances specified on the customer drawing reflect the geometry in dry condition and do not consider humidity and temperature effects.
A specific agreement / specification between manufacturer and customer is required if certain dimensions including tolerances must be guaranteed under environmental conditions in the storage phase or the application (e.g. high humidity and / or temperature).

For the mounting of PCBs, it should be noted that the rated data relates only to the PCB components alone.
The necessary creepage and clearance paths must be observed in connection with the respective applicant in accordance to IEC 60664-1 (VDE 0110).
The current-carrying capacity and pitch tolerance is to be determined according to DIN IEC 60326-3 very fine.

Weidmüller PCB components are tested to the IEC 60947-7-4 standard, and are valid for its field of application.
Provided that the components are used to the intended purpose, all requirements with respect to the occurring of electrical, mechanical, thermic and corrosive stress will be satisfied.

Hole pattern

Pole: 1 2 3 4...

Pole: 1 2 3 4...

D

12	82.50	3.248
11	75.00	2.953
10	67.50	2.657
9	60.00	2.362
8	52.50	2.067
7	45.00	1.772
6	37.50	1.476
5	30.00	1.181
4	22.50	0.886
3	15.00	0.591
2	7.50	0.295
n	L1 [mm]	L1 [inch]
Poles		

P = 7.50
0.295" (Pitch)
D = Ø2 +0.1
0.079"
d = 1.5x0.8
0.059" x 0.031"
l = 5.0 +0.2 -0.6
0.197"
n L1
Poles [mm] [inch]

General Tolerances: .

Changes: EC00008632

1

Mat. No. (SAP) 247210000

Drawings Assembly

Drawn Xiang, Keqin



Responsible Xiang, Keqin

Approved Xu, Shary

08.10.2023

Tolerances ISO 8015

61339

8

Drawing no. Index

Scale: 1:1 Sheet 2 / 4

Weidmüller

LLF 7.50.../90...
PCB TERMINAL
LEITERPLATTENKLEMME

