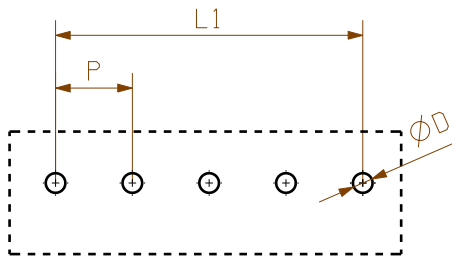
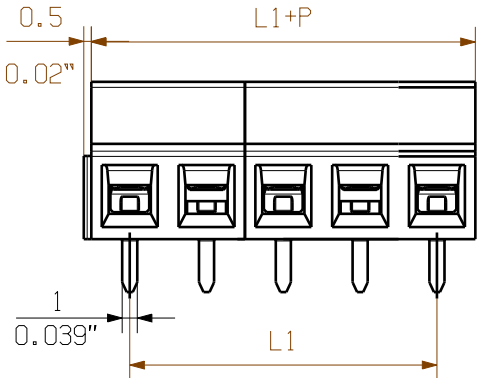
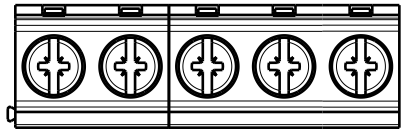
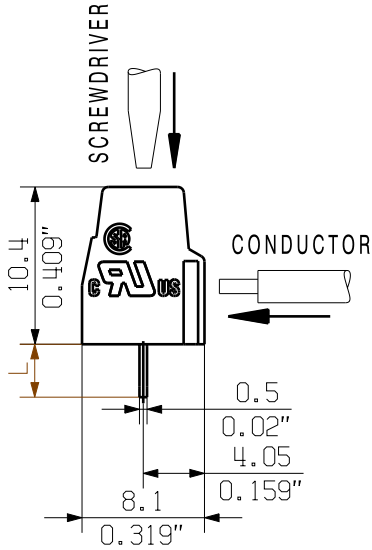


WEITERGABE SOWIE VERVIELFÄLTIGUNG DIESES DOKUMENTS, VERWERTUNG UND MITTEILUNG SEINES INHALTS SIND VERBOTEN, SOWEIT NICHT AUSDRUECKLICH GESTATET.
ZUWIDERHANDLUNGEN VERPFLICHTEN ZU SCHADENERSATZ. ALLE RECHTE FUER DEN FALL DER PATENT-, GEBRAUCHSMUSTER- ODER GESCHMACKSMUSTEREINTRAGUNG VORBEHALTEN.
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PCB LAYOUT



KUNDENZEICHNUNG
CUSTOMER DRAWING

12	55.88	2.20
11	50.80	2.00
10	45.72	1.80
9	40.64	1.60
8	35.56	1.40
7	30.48	1.20
6	25.40	1.00
5	20.32	0.80
4	15.24	0.60
3	10.16	0.40
2	5.08	0.20
N	L1 [mm]	L1 [inch]

For the mounting of PCBs, it should be noted that the rated data stated here relates only to the PCB components alone.
The necessary creepage and clearance paths must be observed in connection with the respective applicant in accordance to IEC 664 / VDE 0110.
The current-carrying capacity and pitch tolerance is to be determined according to DIN IEC 326 part 3 very fine.

Weidmüller PCB components are tested to the DIN EN 61984 standard, and are valid for its field of application.
Provided that the components are used to the intended purpose, all requirements with respect to the occurring of electrical, mechanical, thermic and corrosive stress will be satisfied.

METRIC TOLERANCES				CAT.NO.:	
X.	= ±0.3	61484/5 22.12.11 SHI_S	01	Weidmüller 	C 41703 
X.X	= ±0.1				
X.XX	= ±0.05				
MODIFICATION					
		DATE	NAME	LS 5.08/.../90 3.5 ... LEITERPLATTENKLEMME PCB TERMINAL	
	DRAWN	01.04.2005	ZHOU_N		
	RESPONSIBLE		XU_S		
	CHECKED	20.07.2007	SICKES_A		
SCALE: 2/1		APPROVED	XU_S	PRODUCT FILE: LS 5.08	7064
SUPERSEDES: 2 33380/05					
SUPERSEDED BY: .					