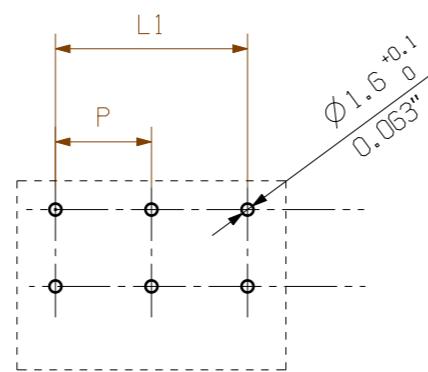
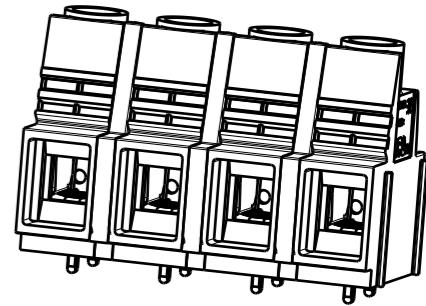
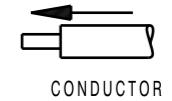
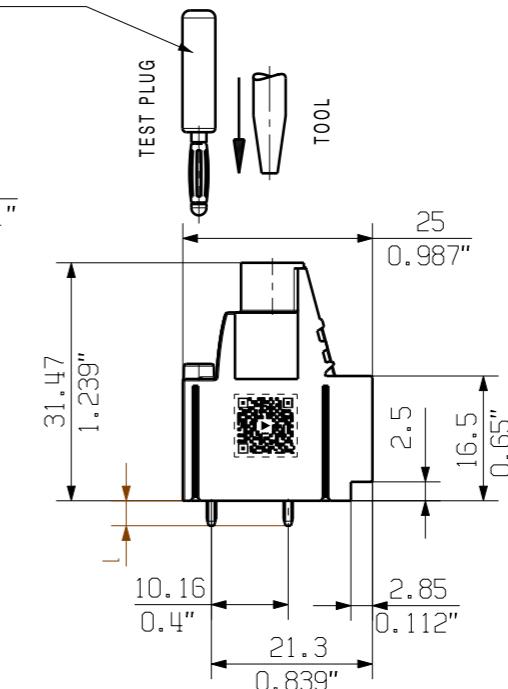
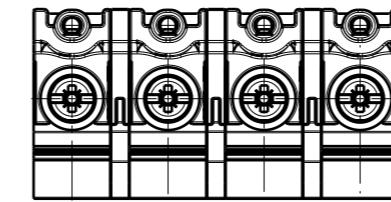
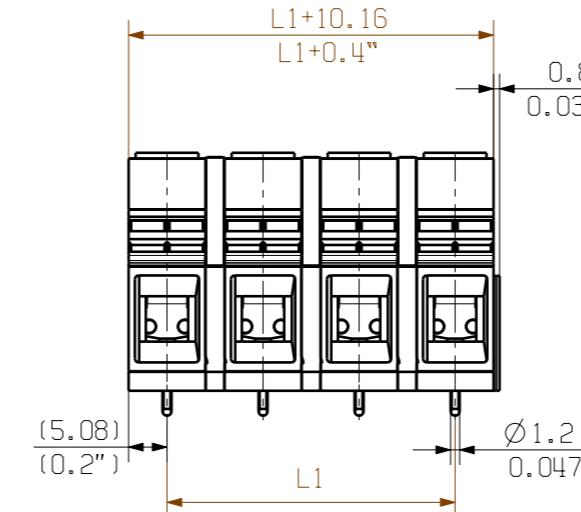
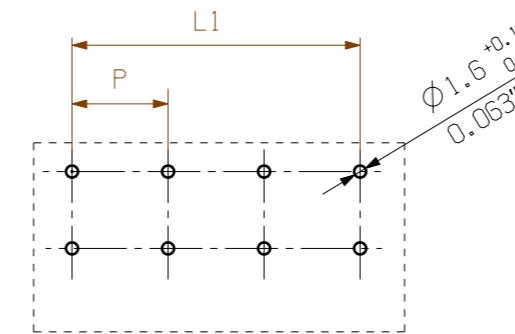


PS 2.0 / ORDER NO.
031000 0000

HOLE PATTERN (BLOCK)



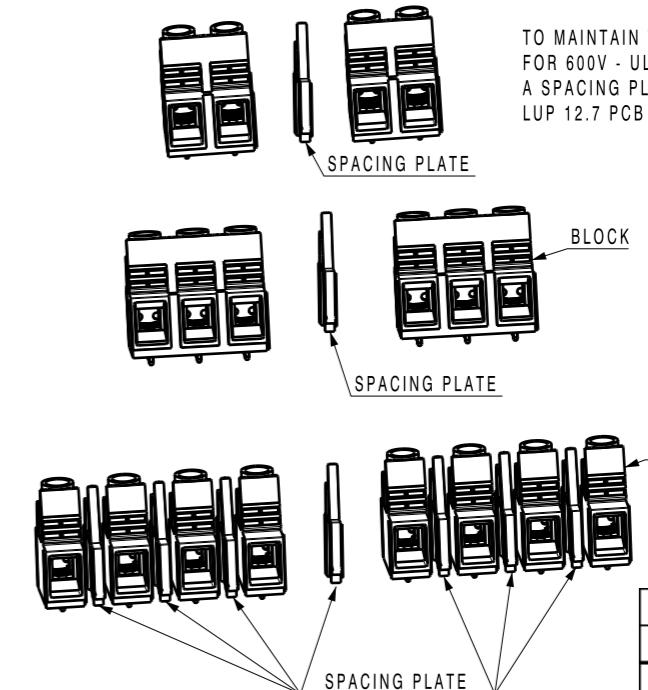
HOLE PATTERN

n = NO OF POLES
 P = PITCH = 12.70
 l = STIFTLAENGE / 5.0mm/0.197"
 PIN LENGTH 3.2mm/0.126"
 SHOWN: LUP 12.70/02/90...SO (BLOCK)
 LUP 12.70/03/90...SO (BLOCK)
 LUP 12.7/04/90... (SINGLE POL)

For the mounting of PCBs, it should be noted that the rated data given in the catalogue relates only to the connection elements. The necessary creepage and clearance paths must be observed in connection with the respective applicant in accordance to VDE 0110. The current-carrying capacity and pitch tolerance is to be determined according to DIN IEC 326 part 3 very fine.

Weidmüller connectors are tested to the DIN VDE 0627 standard, and are valid for its field of application. Provided that the connectors are used to the intended purpose, all requirements with respect to the occurring of electrical, mechanical, thermic and corrosive stress will be satisfied.

TO MAINTAIN THE NECESSARY VOLTAGE DISTANCES FOR 600V - UL & 1000V - IEC, A SPACING PLATE IS REQUIRED BETWEEN ASSEMBLED LUP 12.7 PCB TERMINAL BLOCKS



12	139,70	5,500
11	127,00	5,000
10	114,30	4,500
9	101,60	4,000
8	88,90	3,500
7	76,20	3,000
6	63,50	2,500
5	50,80	2,000
4	38,10	1,500
	5,0	
3	25,40	1,000
2	12,70	0,500
l [mm]	n L1 [mm] L1 [Inch]	

GENERAL TOLERANCE:
DIN ISO 2768-mK

RoHS COMPLIANT	102479	First Issue Date	03.03.2018	Modification		
				Date	Name	
				Drawn	03.03.2018	Administrator
				Responsible		Amann, Alexander
Scale: 2:1	Size: A3	Approved				
Drawings Assembly				Product file: 7233 LUP 10.16/12.7		

Weidmüller 

34163

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Issue no.

Sheet 02 of 02 sheets

LUP12.70/../90

not released